

Government of India
Department of Telecommunication
Telecommunication Engineering Centre
Khurshid Lal Bhawan, New Delhi-110001

No. 5-5/2021-TC/TEC

Dated: 22.05.2023

Notification

Subject: Exemption pertaining to ADSLx & VDSLx Interfaces under MTCTE- reg.

A generic exemption for below mentioned Interfaces/Parameters pertaining to Essential Requirements (ER) of product notified under Mandatory Testing and Certification of Telecom Equipment (MTCTE) is issued up to 30.06.2023 or till further orders.

S. No.	ER Interfaces/Parameters		
	Interface Name	Parameter Name	Standard Name
1	ADSLx	Bit Rate for ADSLx Int	Annex-J1
		Impulse Noise Protection for ADSL Int	Annex-J1
		Insulation Test for ADSL Int	Annex-J1
		Line Port impedance for ADSLx Int	Annex-J1
		Loop resistance for ADSLx	ETSI EN 300 001. Annex-J1
		PSD for ADSLx Int	Annex-J1
		Transmitted Power At ATU-C for ADSLx Int	Annex-J1
	VDSLx	Bit Rate for VDSLx Int	G.993.1 or G.993.2. Annex-J1
		Insulation Test for 2 wire Int	ETSI EN 300 001. Annex-D
		Line Port impedance for VDSLx Int	G.993.1 or G.993.2 Annex-J1
		Loop resistance for VDSLx	ETSI EN 300 001. Annex-J1
		Profiles for VDSLx	G.993.1 or G.993.2 Annex-J1
		PSD for VDSLx Int	G.993.1(cl 6.2). G.993.2(cl 7.2) Ann-A B C. Annex-J1
		Return Loss for VDSLx	G.993.1 or G.993.2 Annex-J1
		Transmitted Power At ATU-C for VDSLx Int	G.993.1 or G.993.2 Annex-J1

This issued with the approval of Sr. DDG, Head of TEC.

Email: adtgtc1-tec-dot@gov.in

1. Sr. DDG & Head, TEC, New Delhi for information please.
2. All DDGs, RTEC
3. C-DoT/IT unit for upload on MTCTE/TEC Website
4. Office copy